Taiwan Semiconductor

0.8A, 200V - 1000V Standard Bridge Rectifier

FEATURES

TAIWAN

• AEC-Q101 qualified available

SEMICONDUCTOR

- Ideal for automated placement
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- UL Recognized File # E-326854
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application

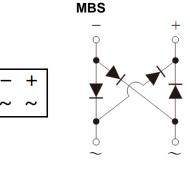
MECHANICAL DATA

- Case: TO-269AA (MBS)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 0.120g (approximately)

KEY PARAMETERS					
PARAMETER	VALUE UNI				
lF	0.8	А			
V _{RRM}	200 - 1000	V			
IFSM	35	А			
T _{J MAX}	150	°C			
Package	TO-269AA (MBS)				
Configuration	Quad				







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)								
PARAMETER		SYMBOL	MBS2	MBS4	MBS6	MBS8	MBS10	UNIT
Marking code on th	e device		MBS2	MBS4	MBS6	MBS8	MBS10	
Repetitive peak rev	erse voltage	V _{RRM}	200	400	600	800	1000	V
Reverse voltage, to	tal rms value	V _{R(RMS)}	140	280	420	560	700	V
Forward current	On glass-epoxy	IF	0.5				А	
Forward current	On aluminum substrate	IF	0.8					А
	d current, 8.3ms single erimposed on rated load	IFSM	35			А		
Rating for fusing (t-	<8.3ms)	l²t	5.08		5.08		A ² s	
Junction temperatu	re	TJ	- 55 to +150			°C		
Storage temperatur	e	Tstg	- 55 to +150			°C		

1



THERMAL PERFORMANCE					
PARAMETER	SYMBOL	ТҮР	UNIT		
Junction-to-lead thermal resistance ⁽¹⁾	Rejl	20	°C/W		
Junction-to-ambient thermal resistance ⁽²⁾	Reja	70	°C/W		
Junction-to-ambient thermal resistance ⁽¹⁾	Reja	85	°C/W		

Notes:

- 1. On glass epoxy P.C.B. mounted on 0.05" x 0.05" (1.3mm x 1.3mm) pads
- 2. On aluminum substrate P.C.B. with an area of 0.8" x 0.8" (20mm x 20mm) mounted on 0.05" x 0.05" (1.3mm x 1.3mm) solder pads

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage per diode ⁽¹⁾	$I_F = 0.4A, T_J = 25^{\circ}C$	VF	-	1	V
Reverse current @ rated V _R per diode ⁽²⁾	$T_J = 25^{\circ}C$	L_	-	5	μA
	T _J = 125°C	I _R	-	100	μA
Junction capacitance per diode	$1MHz, V_R = 4.0V$	CJ	13	-	pF

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE ⁽¹⁾⁽²⁾	PACKAGE	PACKING		
MBSx	TO-269AA (MBS)	3,000 / Tape & Reel		
MBSxH	TO-269AA (MBS)	3,000 / Tape & Reel		

Notes:

- 1. "x" defines voltage from 200V(MBS2) to 1000V(MBS10)
- 2. "H" means AEC-Q101 qualified



100

10

1

0.1

0.01

10 20 30

INSTANTANEOUS REVERSE CURRENT (µA)

CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

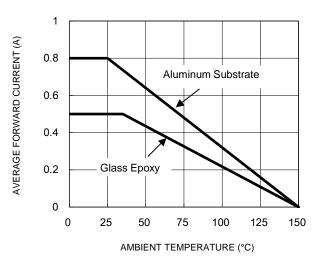


Fig.1 Forward Current Derating Curve

Fig.3 Typical Reverse Characteristics

T_=125 C

T_J=25°C

PERCENT OF RATED PEAK REVERSE VOLTAGE (%)

60 70

80 90 100

40 50

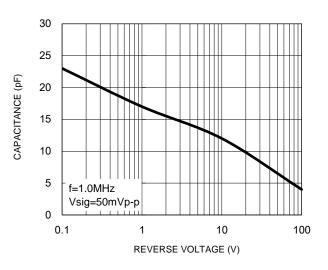
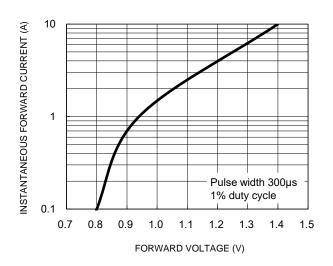


Fig.2 Typical Junction Capacitance

Fig.4 Typical Forward Characteristics

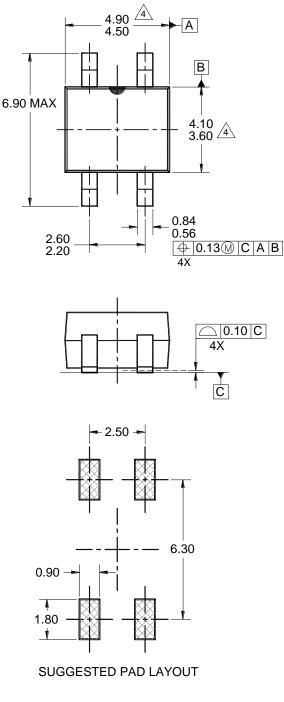


40 PEAK FORWARD SURGE CURRENT (A) 35 8.3ms single half sine wave 30 f = 60Hz25 20 f = 50Hz15 10 5 0 10 100 1 NUMBER OF CYCLES AT 60 Hz

Fig.5 Maximum Non-Repetitive Forward Surge Current

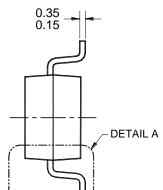


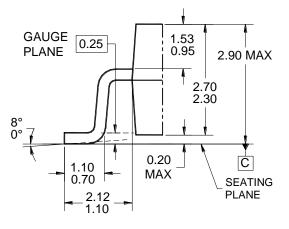
PACKAGE OUTLINE DIMENSIONS



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. PACKAGE OUTLINE REFERENCE: JEDEC TO-269 VARIATION AA.
- A MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 5. DWG NO. REF: HQ2SD07-MBS-089 REV B.









MARKING DIAGRAM

- P/N = MARKING CODE
- YW = DATE CODE
- F = FACTORY CODE

TO-269AA (MBS)



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